

GaAs Constant Gamma Flip-Chip Varactor Diode

Rev. V2

Features

- Constant Gamma for Linear Tuning
- Low Parasitic Capacitance
- · High Q
- Silicon Nitride Passivation
- · Polvimide Scratch Protection
- Surface Mount Configuration

Description

M/A-COM's MA46H120 series is a gallium arsenide flip chip hyperabrupt varactor diode. These devices are fabricated on OMCVD epitaxial wafers using a process designed for high device uniformity and extremely low parasitics. The MA46H120 diodes are fully passivated with silicon nitride and have an additional layer of polyimide for scratch protection. The protective coatings prevent damage to the junction during automated or manual handling. The flip chip configuration is suitable for pick and place insertion.

Ordering Information

| Part Number | Package |
|--------------------|-------------|
| MA46H120-W | Whole Wafer |
| MA46H120 | Gel Pack |
| MAVR-000120-12030W | Waffle Pack |

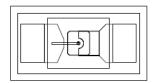
Absolute Maximum Ratings ^{1,2}

| Operating Temperature | -40°C to +125°C | | | |
|-----------------------|-----------------------|--|--|--|
| Storage Temperature | -65°C to +150°C | | | |
| Power Dissipation | 100 mW | | | |
| Mounting Temperature | +235°C for 10 seconds | | | |

- Exceeding any one or combination of these limits may cause permanent damage to this device.
- M/A-COM does not recommend sustained operation near these survivability limits.

Chip Layout

Front View (Circuit Side)

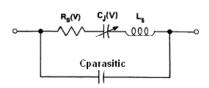


Back View (Operator Side)



Schematic

FLIPCHIP TUNING VARACTOR EQUIVALENT CIRCUIT



Electrical Specifications @ $T_A = +25$ °C

Breakdown Voltage @ $I_R = 10\mu A$, $V_b = 20 \text{ V Minimum}$ Reverse Leakage Current @ $V_R = 14V$, $I_R = 100$ nA Maximum

| | C _T | | C _T | | $\mathbf{c}_{\scriptscriptstyleT}$ | | Q Factor | | Gamma | | | | | | |
|----------|----------------------------|-----|----------------|----------------------------|------------------------------------|-----------------------------|----------|-----------------------------|-------|-----------------------|-----|-----|-----|-----|-----|
| | (pF) | | (pF) | | (p F) | | | | | | | | | | |
| | f=1MHz, V _R =0V | | v=0V | f=1MHz, V _R =4V | | f=1MHz, V _R =10V | | f=50MHz, V _R =4V | | V _R =2-12V | | | | | |
| | Min | Тур | Max | Min | Тур | Max | Min | Тур | Max | Min | Тур | Max | Min | Тур | Max |
| MA46H120 | · | 1.1 | | 0.30 | | 0.40 | 0.14 | | 0.20 | 3000 | | | 0.9 | | 1.1 |

 ^{*} Specifications are subject to change without prior notification

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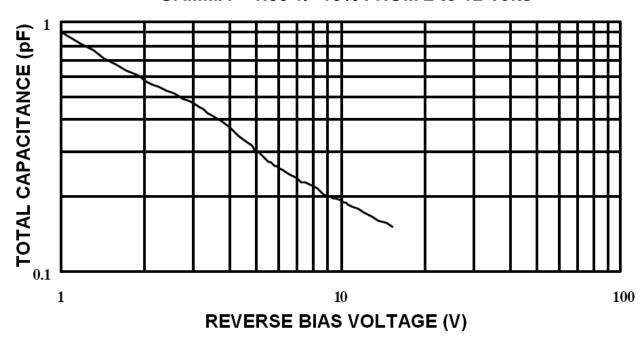


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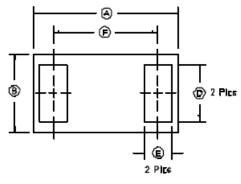
TYPICAL PERFORMANCE CURVES @ +25 °C

CAPACITANCE VS VOLTAGE GAMMA = 1.00 +/- 10% FROM 2 to 12 Volts



CHIP OUTLINE DRAWING

(ODS-1203)



| | |
|-------------|---|
| (C) | |
| • | 1 |
| | |

| | INCH | IES | ММ | | |
|-----|------|------|-----|-----|--|
| DIM | MIN | MAX | MIN | MAX | |
| Α | .025 | .027 | .64 | .69 | |
| В | .012 | .015 | .32 | .37 | |
| С | .006 | .008 | .15 | .20 | |
| D | .007 | .009 | .17 | .22 | |
| E | .004 | .006 | .10 | .15 | |
| F | .018 | .020 | .45 | .50 | |

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ADVANCED: Data Sheets contain information regarding a product M/A-COM Technology Solutions is considering for development. Performance is based on target specifications, simulated results, and/or prototype measurements. Commitment to develop is not guaranteed.

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Solutions has under development. Performance is based on engineering tests. Specifications are
typical. Mechanical outline has been fixed. Engineering samples and/or test data may be available.
Commitment to produce in volume is not guaranteed.

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MA46H120 Series



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Mounting Techniques

These chips were designed to be inserted onto hard or soft substrates with the junction side down. They can be mounted with conductive epoxy or with a low temperature solder preform. The die can also be assembled with the junction side up, and wire or ribbon bonds made to the pads.

Solder Die Attachment

Solder which does not scavenge gold, such as Indalloy #2, is recommended. Sn-Pb based solders are not recommended due to solder embrittlement. Do not expose die to a temperature greater than 235°C, or greater than 200°C for longer than 10 seconds. No more than three seconds of scrub should be required for attachment.

Epoxy Die Attachment

Assembly can be preheated to 125 - 150°C. Use a minimum amount of epoxy. Cure epoxy per manufacturer's schedule. For extended cure times, temperatures must be kept below 200°C.

Handling Procedures

The following precautions should be observed to avoid damaging GaAs Flip-Chips:

Cleanliness

These chips should be handled in a clean environment. Do not attempt to clean die after installation.

Static Sensitivity

Varactor diodes are ESD sensitive and can be damaged by static electricity. Proper ESD techniques should be followed to when handling these devices.

General Handling

The protective polymer coating on the active areas of these dice provides scratch protection, particularly for the metal airbridge which contacts the anode. Dice can be handled with tweezers or vacuum pickups and are suitable for use with automatic pick-and-place equipment.

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